

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (A)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

ACCREDITED BY NAAC A+



THE

ECE

PRESAGE

VOLUME 1
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ABOUT THE UNIVERSITY

Jawaharlal Nehru Technological University Kakinada is formed after quadrifurcating JNTUH in the year 2008. University College of Engineering, Kakinada, is constituent college of this JNTU Kakinada, playing its role of imparting technological education and actively involving in research in the state of Andhra Pradesh since 1946. It is located in the coastal districts of Andhra Pradesh having 159 Engineering, Pharmacy and Management Colleges affiliated to this University.

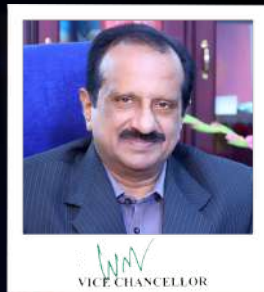
ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering in this Institute is the first department established in the state of Andhra Pradesh as Telecommunication Engineering Department in 1958 and is one of the oldest in the country, and later renamed as Department of Electronics and Communication Engineering. The department has expertise in Communication Engineering, Signal and Image processing, Antennas and VLSI Design. The Department of Electronics and Communication Engineering, University College of Engineering, JNTU Kakinada has contributed many stalwart to serve the society through their committed academic legacy. Both the Department of Electronics and Communication Engineering and its alumni are mutually proud of their achievements. The department has celebrated Platinum Jubilee Year in 2021. The Department of Electronics and Communication Engineering has well qualified and experienced faculty with excellent infrastructural facilities. The Department offers both Under Graduate and Post Graduate program and also the Department supported to offer the university, international collaborative program (ICP) in association with Blekinge Institute of Technology, Sweden (BTH). The department faculty guided and guiding number of research scholars in various fields of specializations. The Department is known for organizing academic interactions through conferences, seminars, symposiums and workshops.



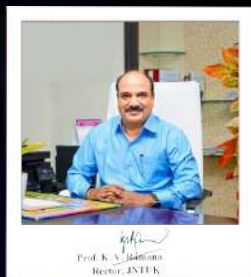
X6HR+4QQ, Jawaharlal Nehru Technological University, Kakinada, Andhra Pradesh 533003

VICE CHANCELLOR'S MESSAGE



Greetings and Hearty Congratulations to Head, Faculty and Students of ECE on the occasion of release of Department Magazine, ECE presage. It is good sign to launch department level magazine first of its kind in the department of ECE . We are proud that UCEK is one of the First institution to have started the branch of Electronics and Communications Engineering in India. UCEK produced high quality world renowned technocrats from ECE like Padmasree Padma Bhushan Dr I Vara Prasada Reddy, Dr VVR Sastry of BEL and many others. At International level also, this branch has produced stalwarts from India like Nandan Nikekani the great Electronics engineer of Infosys and brilliant entrepreneur Satya Nadella, ECE student of Manipal and Now CEO of Microsoft. I am happy that Top global and national recruiters like DRDO, ISRO, Infosys, Wipro, TCE, HCL, etc. are all offering Core jobs for ECE graduates. I understand ECE students are known for their strong analytical, problem solving and technical skills. ECE has a glorious future in view of the deployment of 5G and future 6G in the coming years. I am confident that the Department of ECE will continue to excel in its endeavors, paving the way for a bright and prosperous future. Best wishes for all your future endeavors.

RECTOR'S MESSAGE



I would like to Extend warm congratulations to the entire ECE community at UCEK on the landmark release of "ECE Presage," the department's inaugural magazine. This achievement not only marks a significant milestone for the department but also reflects its pioneering spirit as one of the first institutions to establish Electronics and Communications Engineering in India. Celebrate the illustrious legacy of ECE graduates. Highlight the prestigious opportunities awaiting ECE graduates from top recruiters. As a proud alumni of ECE, UCEK, I take immense pride in the remarkable achievement of ECE students who secured a total of 65 GATE ranks in GATE-2024. I extend my heartfelt best wishes to the Head of the department, faculty members, students, non-teaching staff, and all other stakeholders.

REGISTRAR'S MESSAGE



I am Delighted in the unveiling of "ECE Presage," the departmental magazine curated by the Department of Electronics and Communications Engineering. I recognize this publication as not only a testament to the department's progress but also as a nurturing ground for student creativity and expression. I encourage readers to explore the magazine's pages, where they will discover the department's remarkable journey and the profound insights shared by its budding talents. I emphasize the magazine's potential to serve as a valuable resource for future students, guiding them in their academic and career pursuits. I extend heartfelt gratitude to the dedicated individuals behind the magazine's creation, including the Head of the Department, Magazine Committee Members, faculty, staff, and students, whose collective efforts have brought this edition to fruition.

PRINCIPAL'S MESSAGE




VICE-PRINCIPAL

It gives me an immense pleasure to note that the Department of Electronics and Communications Engineering is bringing out the departmental magazine “ECE PRESAGE”, and provides a platform for every student to develop his learning skills through magazine. As you scan through the pages, it will enlighten you with the important milestones that the department has achieved. Besides, our budding talents have expressed their thoughts, ideas, hopes, feelings, aspirations and convictions in a creative way. This magazine should be a good source of guidance for coming batches of students in choosing activities of their choice in their future for building their carrier. My sincere appreciations to the Head of the Department, Magazine Committee Members, Teaching and Non-Teaching Staff, Students and other stake holders of the Department for bringing this edition of Magazine.

VICE PRINCIPAL'S(ADMIN) MESSAGE



I am thrilled to unveil PRESAGE, the vibrant heartbeat of the ECE department. Within its pages lie the kaleidoscope of our student's creativity, where budding writers find their voice and talented artists paint their dreams. The dedicated magazine team has meticulously crafted each page, weaving together the diverse thoughts and dreams of ECE student community. Their tireless dedication, fueled by the unwavering support of the University/College and the infectious passion of our students, has resulted in a publication that truly resonates with the essence of our department. As you delve into this edition of ECE PRESAGE, we invite you to embark on a journey of discovery, where the hidden talents of ECE students shine brightly. Prepare to be inspired as you witness the boundless creativity and unwavering spirit that defines the ECE Department.

VICE PRINCIPAL'S(ACADEMICS) MESSAGE



I am delighted to present to you the PRESAGE, the ECE department Magazine. PRESAGE serves as an exceptional platform for aspiring writers and talented artists, providing them with an avenue to showcase their creativity. The magazine team has dedicated extensive time and effort to compile the thoughts and dreams of the students, which is truly commendable. None of this would have been possible without the unwavering support of the university and the enthusiasm and passion of the students towards their craft. Their dedication in revealing the hidden talents of their peers is truly inspiring. Hope you enjoy this edition of ECE PRESAGE and appreciate the talent and creativity of the students of ECE Department.

HOD'S MESSAGE

On behalf of the Electronics and Communication Engineering Department, University College of Engineering Kakinada, JNTUK, I am delighted to announce the release of the inaugural volume and issue of our departmental magazine, "ECE PRESAGE." This moment marks the realization of a long-awaited dream, and I am happy to share it with everyone. "ECE PRESAGE" will serve as a comprehensive record of the



significant milestones achieved by our department in this quarter. Established in 1958, our department stands as a testament to the enduring legacy of JNTUK. Over the years, it has been a beacon of dynamic progress and innovation within our institution. We take immense pride in our department's accreditation by the NBA since 2011, a testament to our commitment to excellence in engineering education. Our strength lies in our experienced and highly qualified faculty, dedicated to both teaching and knowledge enhancement. Faculty members regularly participate in seminars, workshops, and summer schools to stay updated with the latest advancements in their fields. Encouraging a culture of research and academic excellence, our faculty and students actively engage in presenting papers at conferences, garnering recognition for our college and department. This research experience significantly contributes to shaping the future of our students.

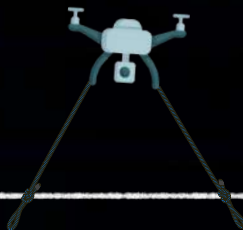
Furthermore, our department boasts excellent placement opportunities, with this year's final year students securing 38 placements thus far, along with a total of 65 GATE ranks achieved by our ECE students. Beyond academics, our students exhibit exemplary behavior and discipline in the classroom, while also actively participating in various non-technical, technical, and cultural activities through clubs and societies such as IETE, Professional Society, and ESparX Technical Fest. I have full confidence in the capabilities of our team, comprising faculty, staff, and students. "ECE PRESAGE" is intended to serve as a guiding resource for both faculty and future student cohorts, aiding them in selecting activities aligned with their interests and career aspirations.

I extend my gratitude to all student members, faculties, industry experts, alumni, students and stake holders for their collective efforts, which I am certain will drive further progress in our field at both national and international levels.

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VISION/MISSION



VISION

To remain a symbol of pride in the fields of Electronics and Communication Engineering by producing holistic and diligent Engineers for industrial and societal needs.

MISSION

M1: To produce high quality learners who are globally competitive and professionally challenged in the field of Electronics and Communication Engineering.

M2: To offer educational programmes that imparts inventive knowledge with high levels of ethical and human values.

M3: To provide a platform to acquire and implement innovative ideas in research and development.

M4: To build up the state of art laboratories and centres of excellence in different areas of Electronics and Communication Engineering.

M5: To train the students and faculty to update their knowledge in pioneering technologies to meet industrial requirements.

MAJOR COURSES

UNDER GRADUATION

- B.Tech (Electronics and Communication Engineering)

POST GRADUATION

- Computers & Communications
- VLSI & ES
- Instrumentations & Control Engineering

TOTAL STUDENTS STRENGTH - 289

	YEAR	STRENGTH	LF	NRI	PMSS	TOTAL
UG	2023 BATCH I YEAR	55	-	-	02	57
	2023 BATCH II YEAR	46	13	01	-	60
	2023 BATCH III YEAR	53	05	01	-	60
	2023 BATCH IV YEAR	52	04	-	-	56
	TOTAL STRENGTH	206	22	03	02	233
PG	M TECH (VLSI & ES)	I YEAR	22	II YEAR	17	
	M TECH (C & CE)	I YEAR	11	II YEAR	02	
	M TECH (I & CE)	I YEAR	02	II YEAR	02	
			35	21		
					TOTAL STRENGTH - 56	

PROGRAMME EDUCATIONAL OBJECTIVES (PEO'S) - UG

PEO1: Do extremely well in professional career and higher education by attaining knowledge in mathematical, computing and engineering principles.

PEO2: Analyze real-life problems, and design systems appropriate to its solutions in the field of Electronics and Communication Engineering that are technically sound, economically feasible, and socially acceptable.

PEO3: Possess good communication skills and ethical attitude with ability to work in teams and adapt to current trends by engaging in lifelong learning.

PROGRAMME OUTCOMES (PO'S) - UG

PO1: Engineering knowledge: Apply the knowledge of Mathematics, Science, Engineering Fundamentals, and an Engineering Specialization to the solution of Complex Engineering Problems.

PO2: Problem analysis: Identify, Formulate, Review Research Literature, and analyze complex Engineering Problems reaching substantiated conclusions using first principles of Mathematics, Natural Sciences, and Engineering Sciences

PO3: Design/development of solutions: Design solutions for complex Engineering Problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, Societal, and Environmental considerations.

PO4: Conduct investigations of complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

PO5: Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.

PO6: The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

PO7: Environment and sustainability: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

PO8: Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

PO9: Individual and team work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

PO10: Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11: Project management and finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO12: Life-long learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

PROGRAMME EDUCATIONAL OBJECTIVES (PEO'S) - PG COMPUTERS AND COMMUNICATIONS

PEO 1 : Acquire technical knowledge for specialized careers in computers, communications and related fields that cater to the Global need.

PEO 2 : Expertise in carrying out project works in advanced communication engineering by using state-of-the-art computing techniques and to develop interdisciplinary research.

PEO 3 : Possess good communication and presentation skills with ability to work in teams and contributing massively to the technological development of Nation.

PROGRAMME OUTCOMES (PO'S) - PG COMPUTERS AND COMMUNICATIONS

PO1: An ability to independently carry out research/investigation and development work to solve practical problems.

PO2: An ability to write and present a substantial technical report/document.

PO3: Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.

PO4: Equip with modern computational and hardware tools for designing communication systems.

PO5: Apply appropriate software tools and operating systems to analyse and solve communication engineering and computer related issues.

PO6: Function on multidisciplinary technological issues assimilating advancements in computers and communications.



PROGRAMME EDUCATIONAL OBJECTIVES (PEO'S) - PG

VLSI & ES

PEO 1: To enrich students to excel in research leading to cutting edge technology in VLSI design and embedded systems and creating competent, innovative, and productive professionals in this field.

PEO 2: To understand the various applications and employ embedded systems to find solutions to them with good scientific and engineering knowledge so as to comprehend, analyze, design, and create novel products and solutions for the real life problems.

PEO3: To inculcate entrepreneurial skills in starting industries applying embedded system technologies.

PROGRAMME OUTCOMES (PO'S) - PG

VLSI & ES

PO1: An ability to independently carry out research/investigation and development work to solve practical problems.

PO2: An ability to write and present a substantial technical report/document.

PO3: Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.

PO4: An ability to design and conduct experiments, perform analysis and interpret the problems of VLSI design.

PO5: An ability to demonstrate the design of an embedded system, component or process as per needs and specifications.

PO6: Students should be able to develop skills to use modern engineering tools, softwares and equipments to analyze problems.



PROGRAMME EDUCATIONAL OBJECTIVES (PEO'S) - PG

INSTRUMENTATION AND CONTROL ENGINEERING

PEO 1: Attain technical knowledge for specialized careers in instrumentation, control systems and related fields that cater to the Global needs.

PEO 2: Expertise in carrying out project works in advanced instrumentation and process control engineering by using recent advancements to do research

PEO3: Possess good communication and presentation skills with ability to work in teams and contributing massively to the technological development of Nation.

PROGRAMME OUTCOMES (PO'S) - PG

INSTRUMENTATION AND CONTROL ENGINEERING

PO1: An ability to independently carry out research/investigation and development work to solve practical problems.

PO2: An ability to write and present a substantial technical report/document.

PO3: Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.

PO4: An ability to independently carry out research/investigation and development work to solve practical problems.

PO5: An ability to write and present a substantial technical report/document.

PO6: Students should be able to demonstrate a degree of mastery over the area as per the specialization of the program. The mastery should be at a level higher than the requirements in the appropriate bachelor program.



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Artisan



Smt. B. Chittamma **Attender**



Sri. G.S.P. Mukharjee

Artisan



Sri. B. Guru Murthy **Cleaner**



Smt. Rama laxmi

Artisan



Smt. B. Devi **Attender**



Smt. P. Madhavi **Technical Assistant**



Sri. K. Venkat Rao **Attender**



Sri. Nageswaro **Technical Assistant**



S. Madhu **Sweeper**



Sri. Venkatesh **Technical Assistant**



Sri. R. Sridhar **Technical Assistant**

FACILITIES & INFRASTRUCTURE

E CLASS ROOM



DEPARTMENT LIBRARY



SEMINAR HALL



FACILITIES & INFRASTRUCTURE (UG)

VLSI & ECAD LABORATORY



PDC & IC LABORATORY



MPMC / C / IOT LABORATORY



ANALOG / DIGITAL COMM. LABORATORY



EDC / DIGITAL ELECTRONIC LABORATORY





FACILITIES & INFRASTRUCTURE (PG)

ADVANCED COMM. / CN LAB



VLSI LAB



INSTRUMENTATION LABORATORY



**ESD / ERTOS / SD WITH
EMBEDDED LINUX LAB**



R & D LAB



DEPARTMENT ACTIVITIES

Expert talk was delivered by **Prof. K. Phani Srinivas**, on "Coronary heart disease detection using Deep Learning Techniques" at the Department of Electronics and Communication Engineering Department for PG 1st and 3rd semester students on 2-1-2024. 40 students have attended the same.

Department of ECE, organized a **one day work shop** on "Artix7 FPGA Board" at ECE Department in collaboration with AVL Technologies on 03-02-2024 which was coordinated by **Smt.L.Pushpa Latha** , Assistant professor and Assisted by Mr.S.Nagarjuna , Assistant professor (C).



Clean and Green Programme in Department NSS activity was organized by the Department of Electronics and Communication. 48 students had participated on 05-01-2024.

**OUR FACULTY PARTICIPATED
IN 10th CONVOCATION**



DEPARTMENT ACTIVITIES

An enlightening expert lecture titled " **VLSI fabrication process and clean room facility** " which is exclusively present only at IISC Bangalore in the entire Country by the renowned explorer Dr. Y.P.Prabhakara Rao, **Visiting Scientist at the Centre for Nano Science and Engineering(CeNSE), Indian Institute of Science, Bangalore** was conducted for Faculty, UG/PG and Ph.D Scholars. 40 students have attended the same on 5-2-2024.



A **Tree plantation drive** was carried out at the Garden space of the Electronics and Communication Engineering on 28-01-2024 in association with One Step India organization as they have donated **70 plants**.

An interactive session with PG students by **Mr. Rajesh Kumar** was organized in online mode in the Department of ECE on **AI based Algorithms** on 6-2-2024.

INTERNATIONAL WOMEN'S DAY CELEBRATIONS 8-3-2024



ALUMNI OF ECE DEPARTMENT



Vamsi Mohan Reddy
and Lakshmi Priya V
Cisco Systems Ltd.
CANADA

I Krishna Rao ,Director All India Radio
V Kumar, GM in Wipro Limited, Hyderabad
Prasad E, Project Manger – QATAR
Eswar E, Software Engineer, Google USA
Krishna V, Sr. Distinguished Engineer, Palo Alto Network

MAJOR EVENTS (UG)



EXPERT TALK WAS DELEVERED BY **PROF. K. PHANI SRINAVAS**, ON **"RESESRCH CHALLENGES PLUS ABOUT CENTRE OF EXCELLENCE"** ON 26-02-2024.

EXPERT TALK BY **DR. Y.P.PRABHAKARA RAO** ON **VLSI FABRICATION PROCESS**



CULTURAL ACTIVITIES



INDUSTRIAL VISIT



4th YEAR STUDENTS OF 40 MEMBERS VISITED **ATC VIJAYAWADA** ON 26-03-2024.

MAJOR EVENTS (PG)

TWO DAY WORKSHOP ON PCB DESIGN



TWO DAY HANDS ON WORKSHOP ON ADVANCED COMMUNICATION INSTRUMENTS



OPEN BOOK ASSIGNMENT



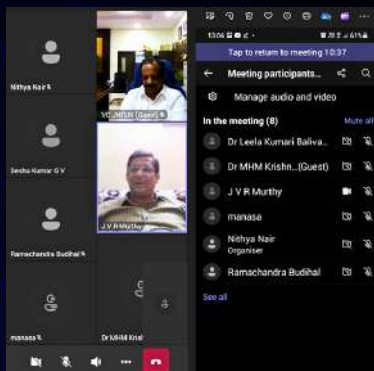
HANDS ON WORKSHOP ON MOBILE TRAINER AND OPTICAL FIBER SYSTEM



GUEST LECTURE BY DR.KRISHNA KANTH G AVALUR VISITING PROFESSOR DIRECTOR OF ANALOG MIXED-SIGNAL R & D CSA ON 20-1-2024



INDUSTRY INSTITUTE INTERACTION



WIPRO-JNTUK Connect(Industry academic interaction) – R&D Project

Industrial visit to AIR TRAFFIC CONTROL (ATC) Vijayawada by final year students on 6-02-2024.



Dr. Leela Kumari Balivada acted as **resource person for National level FDP** during 25-02-2024 to 29-02-2024 organized by GMRIIT.

Invited to SVEC(A) as Chief Guest on the occasion of International Women's Day and delivered a lecture on "Invest in Women, accelerate progress".

Dr. M.Sailaja, Dr. B.T.Krishna and Dr. K Padma Priya have participated as Delegates for International Conferences on "Role of AI in Higher Education" (industry expectations and educator's challenges) during 22-02-2024 to 23-02-2024.



THESIS/DISSERTATION

8 thesis/dissertations were awarded to **Ph.D. students** under the guidance of faculty members in the Department of Electronics and Communication Engineering (ECE).

34 thesis/dissertations were awarded to **M.Tech students** under the guidance of faculty members in the Department of Electronics and Communication Engineering (ECE).

PAPER PUBLICATIONS

Dr.Avala Mallikarjuna Prasad, "Wide Band Flexible Antenna For Future V2X And 5G Automotive Vehicular Imaging And Position Prediction Applications", International Journal of Intelligent Systems and Applications in Engineering, Jan 2024.

Dr.Vinay Kumar, "Efficient automated method to extract EOG artefact by combining Circular SSA with wavelet and unsupervised clustering from single channel EEG ,Biomedical Signal processing and control", Biomedical Signal Processing and control, Volume 87, Jan 2024.

Dr K Rajasekhar, "Design of DNG Met material loaded Single fed Wideband Micristrip Patch Antenna at 28 GHz for Millimetre wave Applications", Tuijin Jishu/Journal of Propulsion Technology, Jan 2024.

Dr K Rajasekhar, "An intelligent lung nodule segmentation framework for early detection of lung cancer using an optimized deep neural system", Multimedia Tools and Applications, Dec 2023.

Dr. R. Madhu, "Image Sharpening and Edge detection using Modified Non-scalar adder model", 3rd International Conference on emerging Frontiers in Electrical and Electronic technologies (ICEFEET), 21-22 December, 2023.

FACULTY ACHIEVEMENTS



Dr. S. Srinivas Kumar served as the Hon'ble Vice Chancellor of JNTUA from December 8, 2017, to December 7, 2020. He currently holds the position of Professor at JNTUK.

Dr.K Padma Raju currently serves as the Hon'ble Vice Chancellor of Adikavi Nannaya University and is a former member of the Andhra Pradesh Public Service Commission (APPSC).



- **Dr. B. Leela Kumari** was sanctioned a research grant of Rs. 2.00 lakhs as seed funding by JNTUK on 26-02-2024.
- **Dr. B. Leela Kumari** has been invited as a Resource Person for a Five-Day Faculty Development Program (FDP) on Cutting-Edge Technologies of Wireless Communications, Signal, and Image Processing (CTWSI-2024), scheduled from 25-02-2024 to 29-02-2024.
- **Dr. B. Leela Kumari** Published a patent "Ergonomic Sitz Bath Tub Chair for Nonsurgical Medical Treatments" on dec 2023.

- **Dr. B.T. Krishna** authored a textbook titled "Linear Integrated Circuits," which has been approved by the AICTE on feb 2024
- **Dr. B.T. Krishna** was awarded a research funding of an amount of Rs.6.00 lakhs from the IIT Guwahati Technology Innovation and Development Foundation for the project titled "Design and Implementation of Fractional Order Circuits for Underwater Applications."



FACULTY ACHIEVEMENTS



Dr. K. Padma Priya served as a reviewer for the AICTE-approved textbook titled "Linear Integrated Circuits" in February 2024.

Dr. K. Rajasekhar was sanctioned a research grant of Rs. 2.00 lakhs as seed funding by JNTUK on 26-02-2024.



Dr. Y. Gowtami was Awarded Ph.D from KL University in 17-02-2024.

Published a patent titled "Design of High-Speed Multiply and Accumulation Unit for Digital Signal Processing Applications" on 08/03/2024 by **Smt. T.V. Ramani**.



STUDENTS ACHIEVEMENTS

PLACEMENTS

20021A0445



NIVAS BODAPATI

PACKAGE OF **35 LPA**
COMMVault SYSTEMS

SOMU NARESH

PACKAGE OF **9 LPA**
TCS PRIME



20021A0422

38 STUDENTS GOT PLACED AND
65 STUDENTS ACHIEVED RANKS IN GATE 2024

COMPANY NAME	STUDENTS PLACED
TCS	10
ACCENTURE	5
GALAXE	5
VEM TECHNOLOGIES	6
MEDHA	6

COMPANY NAME	STUDENTS PLACED
MOSCHIP	3
VOZIQ AI	1
COMMVault	1
APXOR	1

HIGHEST NPTEL COURSES



GOWTHAM

20021A0414
7 COURSES



B KOUSIK

21021A0446
7 COURSES

ELIGIBLE STUDENTS FOR HONORS & MINORS

- **20 STUDENTS** ARE ELIGIBLE FOR HONORS & MINORS FROM 2ND YEAR **2022** ADMITTED BATCH.
- **30 STUDENTS** ARE ELIGIBLE FOR HONORS & MINORS FROM 3RD YEAR **2021** ADMITTED BATCH.

STUDENTS ACHIEVEMENTS



UNIVERSITY GOLD MEDAL FOR FIRST IN ECE DEPARTMENT



K.SAI SUDHEER

19021A0409

CGPA : 9.43

TEAM LEADER OF TEAM RAIDERS IN NATIONAL SMART INDIA HACKATHON'23

LAKSHMI NARAYANA

22021A0410

SECOND POSITION



FIRST MINOR ENGINEERING DEGREE IN CSE BY ECE STUDENT



Y.L.SAI KRISHNA

19021A0409

SECURED 2ND PRIZE IN ELEVATE ELEVEN PROGRAM CONDUCTED BY 94-98 ALUMNI

C LAAVAN KUMAR	20021A0430
BONDILI SANDEEP SINGH	20021A0405
PEDDIREDDY YOSHITHA	20021A0444
KOTHEM HRUSHIKESH	20021A0418

AND ALSO PARTICIPATED IN VEDHA'S PAPER PRESENTATION AT ADITYA SURAMPALEM BY YOSHITHA & HRUSHIKESH.



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S.DILEEP KUMAR (M.TECH)
HAS CLEARED THE PRELIMS & MAINS AND ATTENDED FOR THE UPSC-2023 INTERVIEW.

GATE RANKS 2024

TOTAL GATE RANKS : 65

RANKS BELOW 1000 : 12

RANKS BELOW 2000 : 22



10th RANK

Y LAKSHMI SAI KRISHNA



RANK 152

P JAGADEESH



RANK 189

AVSS GOWTHAM



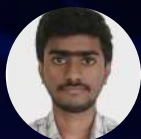
RANK 289

SATYA SAI REDDY



RANK 359

THOTA PRASANTH



RANK 432

EMANI SRIRAM



RANK 479

B SANDEEP SINGH



RANK 615

G VINAY KUMAR



RANK 669

GANTA NARASIMHA



RANK 772

SONIKA



RANK 849

G.SATYA NARAYANA



RANK 937

N SREE PRABHATH



RANK 1027

SHAIK VAHEEDA



RANK 1315

KATTA SUREKHA



RANK 1315

N SREE PRANEETH



RANK 1512

P. UMESH REDDY



RANK 1666

K.DIVYA SATYA SRI



RANK 1750

B.YASASWINI



RANK 1750

PRADEEP V



RANK 1750

M PAVAN VIGNESH



RANK 1894

K V SURESH KUMAR



RANK 1993

Y.KAUSHIK

GATE RANKS 2024



RANK 2162
T.S.YASHWANTH



RANK 2445
C.SAI CHARAN



RANK 2682
V.PADMAJA



RANK 2682
P.S.S.SAILAJA



RANK 2790
J.PRIYADARSHINI



RANK 3524
D.D.KEERTHANA



RANK 3524
G.SRI TEJ



RANK 4056
P.S.GAYATRI



RANK 4430
A.RAJESWARI



RANK 4626
K.GNANENDER



RANK 5119
G.V.SATHVIKA



RANK 5119
B.LAHARI



RANK 5843
S.LAKSHMAN



RANK 6400
L.SAI ROHITH



RANK 6433
R.AKHILESH



RANK 6433
E.V.R.D.ANIL



RANK 7092
D.S.LALITH



RANK 7092
CH.GIRIDHAR



RANK 7648
P.SUMANJALI



RANK 7824
J.LIKHITHA



RANK 8212
M.D.SAMEERA



RANK 8212
R.B.S.SRIHARI



RANK 8619
K.YASASWI



RANK 8619
C.YASASWINI



RANK 9912
L.SANTHOSHI

NO. OF RANKS SECURED BY 3rd YEAR STUDENTS : 34

NO. OF RANKS SECURED BY 4th YEAR STUDENTS : 31

PROFESSIONAL DEVELOPMENT

💡 **Dr. B. Leela Kumari** attended an Orientation/Refresher Course organized by **Pantech Solutions** in association with **IETE, India**, from December 11, 2023, to January 10, 2024 at Pantech Solutions.

💡 **Dr. B. Leela Kumari** attended a guest lecture by **Prof. Tulasi Ram Das**, Former VC of JNTUK, on the topic of "**Innovation and Incubation Eco System**" on February 1, 2024 at JNTUK Kakinada.

💡 **Dr. B. Leela Kumari** and **Smt. P. Pushpa Latha** organized a workshop titled "**Hands-on Session on ARTIX 7 FPGA Board**" on February 3, 2024, at the Department of ECE, UCEK, JNTUK.

💡 **Dr. B. Leela Kumari** participated in an industry collaborative research project with WIPRO as part of the **WIPRO-JNTUK Connect R&D Project**. The participation took place during an online meeting on February 14, 2024.

💡 **Dr. K Rajasekhar** organized the second phase of the Orientation and Boot Camp program in association with the **Subbu Kota Foundation** on December 19, 2023, at JNTUK Kakinada.

💡 **Smt. P. Pushpa Latha** attended a **Master Class on Internet of Things** as part of an Orientation/Refresher Course from December 25, 2023, to January 24, 2024, through Pantech e-learning.

💡 **Mrs. A. Rajani** attended an FDP titled "**English Language, Literature, Culture & Policy Initiatives**" organized by **ACT Academy**, Tamil Nadu, from January 17, 2024, to January 24, 2024, at Arasu College of Arts & Science for Women, Karur District, Tamil Nadu.

ENGAGEMENT & SPORTS



- First place in the Central Zone intercollegiate Kho-Kho men and women's tournament, along with university selection trials, for the 2023-2024 season.
- The championship title for the 2023-2024 Intercollegiate Athletics Men's and Women's 4x400 Relay, first leg, was clinched by the first-place team.
- Participated in the South Zone Inter-University Kho-Kho Tournament for Women in the academic year 2023-2024.



G. HARIKA



P. DHARANI

Member of the University South zone badminton team in 2024.



SIDDHARTHA

Member of the University South zone football team in 2023.



S. NARESH

Part of the University central zone volleyball team



B. ABHISHEK

Part of the University central zone football team



N.RAJ KUMAR

Part of the University central zone football team



B. ESWARI

First Place in the Central Zone Inter Collegiate Kho Kho Men's and Women's Tournament (2023-2024)



U. SRI SATYA

B.TECH GALLERY



IV B.TECH



III B.TECH



II B.TECH



I B.TECH





OUR TEAM

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DR. LEELA KUMARI BALIVADA
HEAD OF THE DEPARTMENT

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