DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (A)
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
ACCREDITED BY NAAC A+

THE ECE

THE ECHOS

VOLUME 2
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CONTENTS

VISION/MISSION

VISION

To remain a symbol of pride in the fields of Electronics and Communication Engineering by producing holistic and diligent Engineers for industrial and societal needs.

MISSION

M1: To produce high quality learners who are globally competitive and professionally challenged in the field of Electronics and Communication Engineering.

M2: To of er educational programmes that imparts inventive knowledge with high levels of ethical and human values.

M3: To provide a platform to acquire and implement innovative ideas in research and development.

M4: To build up the state of art laboratories and centers of excellence in dif erent areas of Electronics and Communication Engineering.

M5: To train the students and faculty to update their knowledge in pioneering technologies to meet industrial requirement.

EXPERT'S ARTICLE

VLSI ARCHITECTURE FOR SIGNAL, SPEECH, AND IMAGE PROCESSING

Dr.N BALAJI
Ph.D
Professor of ECE,UCEK

In modern signal, speech, and image processing systems, efficient VLSI architectures are critical for achieving high performance with minimal power and area. Discrete Wavelet Transform (DWT), a cornerstone in applications like JPEG 2000, MPEG-4, and biomedical imaging demands optimized arithmetic units such as multipliers to reduce computational overhead. This article explores two advanced multiplier architectures—Radix-8 Booth and Canonical Signed Digit (CSD)—tailored for lifting-based 2D-DWT implementations. These designs address challenges in area, power, and delay, making them ideal for portable and high-speed systems.

Optimized Multiplier Architectures:

1. Radix-8 Booth Multiplier

Traditional multipliers generate redundant partial products, increasing area and power. The proposed 16-bit Radix-8 Booth multiplier minimizes this by selectively generating partial products based on coefficient requirements. Key innovations include:

- *Reduced Partial Products: By grouping multiplier bits into 3-bit clusters (Radix-8 encoding), the number of partial products is reduced to [n/3][n/3] when considering n-bit numbers, lowering adder complexity.*
- *Guard Bits for Sign Extension: Eliminates sign-extension logic up to (2n−1) bits, simplifying the partial product array (PPA).*
- *Truncation Strategy: Post-truncation of non-essential bits (e.g., higher/lower order bits) reduces hardware resources while maintaining acceptable error (±1% in image processing).*

Synthesis Results: *In 90 nm Cadence Genus, the design achieves 28.22% lower Excess Area-Delay Product (EADP) and 34.97% lower Excess Power-Delay Product (EPDP) compared to conventional Booth multipliers.*

2. Canonical Signed Digit (CSD) Multiplier

CSD representation minimizes non-zero digits in constants, reducing shift-add operations. The proposed CSD multiplier for lifting-based DWT features:

- *Redundant Logic Elimination: Removes unnecessary shifters and multiplexers from Sum-of-Power-of-Two (SOPOT) architectures.*
- *Control Register Optimization: Compact 8-bit control registers streamline shift operations, reducing combinational logic.*
- *Error-Resilient Design: CSD coefficients (e.g., α=−3.172) are encoded as sparse signed terms (e.g., $-2^2+2^0-2^{-2}$), cutting adder stages.*

Synthesis Results: *For 16-bit coefficients, the CSD multiplier achieves 21.75% lower ADP and 22.07% lower PDP compared to SOPOT designs.*

Application in 2D-DWT Architecture :

- *Radix-8 Booth in 2D-DWT: Replaces CSD multipliers in row/column filters, reducing latency by eliminating 4 clock cycles per operation. The Partial Product Array structure (Figure 1) integrates guard bits and carry compensation, enabling a 29.01% EADP improvement over CSD-based designs.*

- *CSD in Lifting-Scheme DWT: Optimizes scaling steps (e.g., di=di(2)/K) by replacing hard multipliers with shift-add units. Parallel processing of prediction/update stages (Figure 2) reduces critical path delays by 12–21%.*

Hardware Utilization:

- *Radix-8 Booth: Uses 802 XOR/XNOR gates and 1,949 combinational cells, reducing register count by 50% versus CSD.*
- *CSD: Requires 35% fewer adders than SOPOT for 9/7 filter coefficients, enhancing throughput.*

Conclusion and Future Scope :

- *The Radix-8 Booth and CSD multipliers offer area-power-delay efficient solutions for DWT-centric applications. Future work includes:*
- *Multi-Level DWT: Extending the architectures for 3D/multi-resolution transforms.*
- *Approximate Computing: Leveraging error resilience for ultra-low-power IoT devices.*
- *AI Integration: Deploying these multipliers in neural accelerators for real-time speech/image processing.*

By merging algorithmic optimizations with VLSI-aware design, these architectures pave the way for next-generation embedded systems.

MAJOR ACTIVITIES

INAUGURATION OF ECE GALLERY HALL

The prestigious Gallery Hall, following its renovation, was inaugurated on January 6, 2025. The event was graced by the Hon'ble Vice-Chancellor, Rector, Registrar, Principal, and Vice-Principal. The President and Convenor, Dr. B. Leela Kumari,HoD of ECE along with other attendees, reminisced about their association with the Gallery Hall and expressed their happiness over its renovation.

DEPARTMENT ACTIVITIES

GUEST LECTURES

Guest Lecture by Dr. Rao V. Mulpuri ,Professor in the Department of Electrical and Computer Engineering, George Mason University, United States on Education and Career Opportunities in the United States.

The lecture offered valuable insights into the U.S. education system, including the application process for higher studies, financial aid opportunities, job market trends, and practical advice for building a successful career in the United States. Research scholars, postgraduate, and undergraduate students attended the program and found it highly beneficial.

Guest Lecture By Prof. KVSG Muralikrishna Motivational Speech and Career Guidance.

A guest lecture was organized under the IEEE Student Branch on January 6, 2025, as part of the inaugural function of the ECE Gallery Hall. The event featured the Hon'ble Vice-Chancellor as the speaker, who delivered an inspiring motivational speech and offered valuable career guidance to research scholars, postgraduate, and undergraduate students.

Guest lecture By Prof. P.Srihari Rao, Department of ECE, NITW
On VLSI DESIGN

Guest LectureGuest Lecture on Intellectual property rights by Dr. Alapati. Srinivasa , advocate , IEEE student branch given lecture. **21/03/2025**

Guest Lecture on Intellectual Property Rights

The IEEE Student Branch organized a guest lecture on Intellectual Property Rights (IPR), delivered by Dr. Alapati Srinivasa, a renowned advocate with expertise in IPR laws. The session aimed to raise awareness among students about the importance and implications of intellectual property in academic and professional domains.

Dr. Srinivasa provided valuable insights into various aspects of IPR, including patents, copyrights, trademarks, and industrial designs. He emphasized the significance of protecting innovations and creative works, especially in the field of science and technology. The lecture also covered real-world case studies and legal procedures involved in filing and securing intellectual property.

Department Magazine

Department magazine Volume-1 Issue4 was released by Dr.K.V.S.G.Murali Krishna on 6th january 2025.The Magazine consists of department activities.

WORKSHOPS

ARDUBOTICS

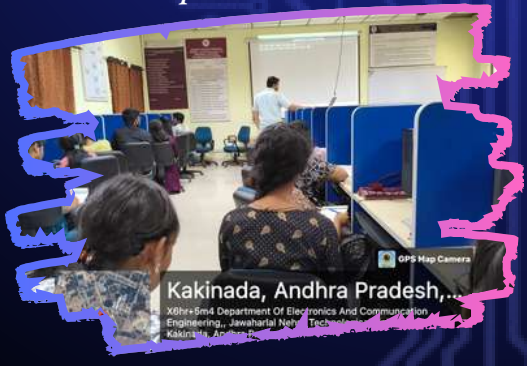
(14/03/2025)-(16/03/2025)

A Three Days Workshop on Ardubotics was held from march 14 ,2025 to March 16,2025 .R Sanakkiyan and Selvakumar from Zeekers pvt. Ltd acted as resource persons and Mr.K.Hari Krishna, Mr.Krishna Prasad and Mr.Ganesh acted as coordinators and Dr. Balivada Leela kumari as convenor . Around 15 batches of students participated in the workshop, each batch consists of 5 members.

CADENCE

(15/03/2025)-(16/03/2025)

A Two Days Workshop on Cadence was held from march 15 ,2025 to march 16,2025 . Sanjay G S from Entuple Technologies pvt Ltd acted as resource persons and Dr.Pushpalatha Pondreti acted as coordinator and Dr. Balivada Leela kumari as convenor . Around 50 students participated in the workshop.



IOT USING RASPBERRY PI

(15/03/2025)-(16/03/2025)

As part of the ECE Department's technical fest, a two-day workshop on IoT using Raspberry Pi was held on March 16th and 17th, 2025. The workshop was hosted by the University College of Engineering, Kakinada, JNTUK, in association with IIT Varanasi and Innovians Technologies. Mr. B. Anvesh Reddy served as the resource person, while Dr. K. Rajashekar and Smt. A. Rajani acted as the faculty coordinators. A total of 77 students participated, making it a highly successful and engaging event.

PCB DESIGNING

(15/03/2025)-(16/03/2025)

As part of the ECE department's technical fest, a two-day PCB Design Workshop was held on March 15th and 16th, 2025. Mr. Chanti from Regol Technologies served as the resource person, while Smt. D. Viswabharati acted as the faculty coordinator, and Dr. B. Leela Kumara served as the convenor. Approximately 35 students participated in the workshop, making it a resounding success.

E⚡PARX 2K25

ESPARX 2K25, a prestigious two-day national-level technical symposium, was organized by the Department of Electronics and Communication Engineering (ECE) at the University College of Engineering (UCEK), JNTUK. The event, held on March 15th and 16th, 2025, provided a platform for students, professionals, and enthusiasts to showcase their technical prowess and engage in a wide range of activities designed to foster innovation and creativity

The symposium was gracefully led by Dr. B. Leela Kumari, who served as the Convener, and Dr. P. Pushpalatha, who took on the role of Faculty Coordinator. Their visionary leadership, coupled with the efforts of the faculty, staff, and student volunteers, made Esparx 2K25 a resounding success. Their dedication and hard work ensured that the event ran seamlessly, offering participants an enriching experience.

ESPARX 2K25

The festivities began with a vibrant opening ceremony, marked by the reveal of the official logo,banner and posters of Esparx 2K25. The event was graced by the presence of the Head of the Department (HOD), faculty coordinators, professors, and a large number of enthusiastic students. The reveal was a significant moment, symbolizing the commencement of an exciting and engaging two days filled with intellectual and cultural activities.

Esparx 2K25 was more than just a symposium, it was an enriching platform for hands-on learning and skill development. A series of highly informative technical workshops were conducted, catering to various aspects of modern technology. The workshops included Ardubotics, Cadence, IoT using Raspberry Pi, LaTeX, and PCB Designing. These workshops attracted participants eager to learn and apply their knowledge in cutting-edge technologies.

It was also home to a series of competitive technical contests that brought out the best of the participants' skills, including Quiz, Paper Presentations, Circuitix, Project Expo, Hackathon, and Picatronics.

ASSET:ASSOCIATION OF STUDENTS FOR SKILL ENHANCEMENT AND TALENT

The Department of Electronics and Communication Engineering (ECE) proudly runs a vibrant and student-driven association called ASSET– Association of Students for Skill Enhancement and Talent. ASSET serves as a dynamic platform that encourages students to explore, express, and evolve by participating in a wide range of academic, cultural, and extracurricular activities. The core objective of ASSET is to awaken the talents and ideas hidden within students and to help them grow both personally and professionally.

Under ASSET, there are several dedicated clubs—Technical, Innovation, Cultural, and Sports—each focused on nurturing different skill sets and passions. These clubs are entirely managed by students, promoting a strong sense of ownership, leadership, and responsibility.

1. Technical Club

21/02/2025

The Technical Club is a powerhouse for students who are passionate about technology and engineering. It organizes workshops, seminars, technical quizzes, coding contests, circuit design challenges, and hands-on sessions in trending technologies like IoT, robotics, AI, and embedded systems. The aim is to bridge the gap between classroom learning and practical implementation, preparing students to face real-world engineering challenges.

2 . Innovation Club

24/02/2025

The Innovation Club encourages students to think creatively and work on practical solutions to real-life problems .It supports project-based learning, organizes idea contests,startup mentoring, and prototype development sessions.A guest Lecture was delivered by Dr. .Leelakumari about Club of Innovation and Entreprenuership.

The club nurtures an entrepreneurial mindset and inspires students to become inventors and changemakers.

3. Cultural Club

01/03/2025

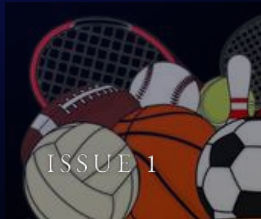
TThe Cultural Club brings vibrancy and artistic flair to the department. It provides a platform for students to showcase their talents in music, dance, drama, painting, photography, and literature. Through events like cultural fests, talent hunts, open mic nights, and traditional celebrations, students gain confidence, overcome stage fear, and celebrate diversity.

4. Sports Club

07/01/2025

The Sports Club encourages physical fitness, team spirit, and healthy competition. Final-year ECE students conduct a sports event for their juniors every year, including indoor and outdoor games such as chess, carroms, badminton, treasure hunt, throwball, and volleyball. These games provide a welcome respite from studies while developing coordination, strategy, and camaraderie.

Student-planned and organized, the event rewards winners with prizes and certificates. The occasion serves as a means to highlight the importance of health, sportsmanship, and team spirit by ASSET. With initiatives like these, ASSET promotes leadership, creativity, and friendship among students so they may develop into responsible and confident personalities.



INDUSTRY INSTITUTE INTERACTION

RELIANCE INDUSTRIES: 15/03/2025

Dr. Venugopal Raju, CEO of Reliance Industries Ltd, addressed the students on industry needs, highlighting the importance of skill development, innovation, and staying updated with emerging technologies. He encouraged students to cultivate a problem-solving mindset and be adaptable to the dynamic nature of the industry. His session served as a valuable bridge between academic learning and practical industry expectations.

AVANTEL COLLOBORATED MOU:

The ECE department launched on the 7th of March an industry-partnership PG programme in Signal Processing and Communications, which sought to bridge academic curriculum and applications. As a part of it, a MOU was planned for the purpose of setting up a Centre of Excellence to ensure research, innovation, and exposure. This endeavour increases Industry-Institute Interaction and provides students with the skillsets matching industry demands.

FACULTY PUBLICATIONS(CONFERENCES/JOURNALS)

Dr. B. Leela Kumari delivered her research paper entitled "Implementation of Lightweight Shufflenet Based CNN for Classification of Arrhythmia for Wearables" during the International Research Conference on Recent Advances in Science, Technology & Engineering conducted on 14th March 2025. This paper was under the category of International Conferences.

Dr. K. Rajasekhar has delivered the paper entitled "Efficient Fractional Order Derivatives Design Based on Frequency Response Based TFE Approach" at International Conference on Advances in Signal Processing and Communication Engineering (ICASPACE-2025) held on 30–31 January 2025.

Dr.P.Pushpa Latha Published Published a patent titled "Echo Sound Based 360 Degrees Surveillance RADAR System", Patent No. 202541008308, Patent Office Journal No. 07/2025 in February 2025.

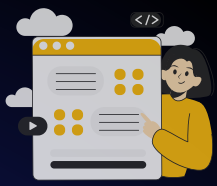
Smt. T.V. Ramani read a paper entitled "Implementation of Lightweight Shufflenet Based CNN for Classification of Arrhythmia for Wearables" in the International Research Conference on Recent Advances in Science, Technology & Engineering on 14th March 2025.

Dr.P.Pushpa Latha Published a patent titled "Echo Sound Based 360 Degrees Surveillance RADAR System", Patent No. 202541008308, Patent Office Journal No. 07/2025 in February 2025.

COMMUNITY ENGAGEMENT

The Department of Electronics and Communication Engineering (ECE) successfully organized Swachh Day on 18th january 2025,15th February 2025 and 22nd march 2025,i.e. every 3rd satursday of month,under the esteemed leadership of Dr. B. Leela Kumari, Head of the Department (HOD), ECE.

The event aimed to promote cleanliness, environmental awareness, and sustainability among students and faculty. Various activities, including a campus cleanliness drive, awareness sessions, and eco-friendly initiatives, were conducted to instill a sense of responsibility toward maintaining a cleaner and greener environment.



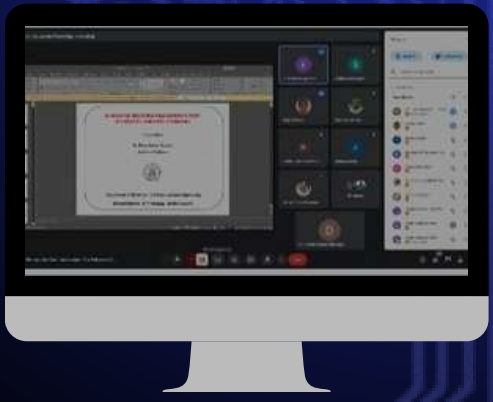
FACULTY DEVELOPMENT PROGRAMME CONDUCTED

ONE WEEK FDP programme

One week FDP on optimization Solutions for CMOS design organized by JNTUK and NIT warangal sponsored by MIETY and Coordinators are B Dr.N balaji,Dr B.leela kumari in addition with Dr P.Pushpa Latha

TWO WEEK FDP programme

Two week FDP on next generation communication in digital world ,from 19thmarch_28th march organized by electronics & ICT Acadmey,NIT warangal in associassion



PROFESSIONAL DEVELOPMENT

Dr. B. Leela Kumari hosted a 5-Day Workshop on Optimized Solutions for IC Design with Hands-on CMOS Design, EI & ICT Spoke Hub Model from February 3 to February 7, 2025. The workshop was held both in online and offline modes, exposing participants to the latest IC design techniques and hands-on practice in CMOS technology.

Dr.B.Leela Kumari conducted a 10-day workshop on "Next Generation Communication: The Future of Connectivity in a Digital World" under EI&ICT Spoke Hub Model from March 19, 2025 to March 28, 2025 in online mode.

Dr.B. Leela Kumari ably rendered service as the Convener of a Two-Day Workshop on Cadence, under the aegis of Esparx, National Level Technical Fest, organized by the Department of Electronics and Communication Engineering, UCEK, JNTUK. The activity took place from 15th to 16th March 2025 and consisted of interactions among students and experts to acquaint them with updates on Cadence tools and uses in VLSI design and embedded systems.

Dr.B. Leela Kumariwas the Convener of a Two-Day Workshop on PCB Design, conducted by the Department of Electronics and Communication Engineering (ECE) under the aegis of Esparx – A National Level Technical Fest at University College of Engineering Kakinada (UCEK), JNTUK, dated 15th to 16th March 2025.

Dr.Leela Kumari has worked as convener of the two-day IoT Application workshop carried out under Esparx banner that is sponsored by Department of Electronics and Communication Engineering (ECE), UCEK, JNTUK. It is organised between the 15th to 16th of March, 2025 and featured fervent registration of students as well as experts throughout the country.

Dr.B.Leela Kumari was the Convenor of a Three-Day Robotics Workshop conducted from 14th to 16th March 2025. The workshop was conducted by the Department of Electronics and Communication Engineering (ECE) under the aegis of Esparx, the National Level Tech Fest at University College of Engineering, Kakinada (UCEK), JNTUK. The program sought to deliver hands-on exposure and knowledge in the emerging industry of robotics, stimulating innovation and technical capabilities in the participants from all over the country.

Dr. N.Balaji hosted a 5-Day Workshop on Optimized Solutions for IC Design with Hands-on CMOS Design, EI & ICT Spoke Hub Model from February 3 to February 7, 2025. The workshop was held both in online and offline modes, exposing participants to the latest IC design techniques and hands-on practice in CMOS technology.

Dr. N.Balaji conducted a 10-day workshop on "Next Generation Communication: The Future of Connectivity in a Digital World" under EI&ICT Spoke Hub Model from March 19, 2025 to March 28, 2025 in online mode.S technology.

Dr. M. Sailaja attended a professional development program entitled "Next-generation Communication: The Future of Connectivity in the Digital World", conducted by the Electronics & ICT Academy. The program was conducted from 19th to 28th March 2025 at the National Institute of Technology (NIT), Warangal.

Dr. B.T. Krishna attended a six-day short-term course on "Analog ICs and Semiconductor Advancements in VLSI" from February 10th to 15th, 2025, at NIT Delhi.

Dr.K.RamaDevi ATTENDED A 5-Day Workshop on Optimized Solutions for IC Design with Hands-on CMOS Design, EI & ICT Spoke Hub Model from February 3 to February 7, 2025. The workshop was held both in online and offline modes, exposing participants to the latest IC design techniques and hands-on practice in CMOS technology.

Dr. K. Jhansi Rani attended a professional training program named "Next-generation communication: the future of connectivity in the digital world," conducted by the Electronics & ICT Academy. The event took place at NIT Warangal during 19th-28th March 2025, and its emphasis was on new developments in communication technologies and how they are contributing to defining the future of digital connectivity.

Dr. P.Pushpa Latha ATTENDED a 5-Day Workshop on Optimized Solutions for IC Design with Hands-on CMOS Design, EI & ICT Spoke Hub Model from February 3 to February 7, 2025. The workshop was held both in online and offline modes, exposing participants to the latest IC design techniques and hands-on practice in CMOS technology.

Smt. A. Rajani ATTENDED a 5-Day Workshop on Optimized Solutions for IC Design with Hands-on CMOS Design, EI & ICT Spoke Hub Model from February 3 to February 7, 2025. The workshop was held both in online and offline modes, exposing participants to the latest IC design techniques and hands-on practice in CMOS technology.

Smt. A. Rajani attended a one-week Faculty Development Program (FDP) on "Mathematical Applications: Computational AI, Neural Networks, and Vedic Mathematics," conducted by ACT Academy. It took place from 11th to 17th March 2025 in Kerala with a prime focus on developing faculty skills to incorporate advanced computational methodologies and classical mathematical techniques into pedagogical practice.

STUDENT ACHIVEMENTS

GATE RANKERS 2025

CH. SAI CHARAN

AIR 9th Rank

PRABATH

Rank:164
In:246

PRASANTH

Rank:268
In:498

RAJESWARI

Rank:425

PAVAN VIGNESH

Rank:468
(In)

SUREKHA

Rank:645

ARUN

Rank-932

TOTAL GATE RANKS : 45
RANKS BELOW 1000 : 7
RANKS BELOW 5000 : 10
RANKS BELOW 10000 : 10
RANKS ABOVE 10000 : 18

PLACEMENTS

Three of our students have been successfully selected by **ANALOG PORTS** with a package of


JYOTHIKA


SUSHMA DEEPIKA


DEEPTHI

APPOLO TYRES has selected ***Eight*** of our talented students with a package of **4 LPA**


MOINUDDIN


HIMA ANITHA


BHUVANESWARI


SATHISH


PRIYANKA


JASMINE


JAFRIN


LAHARI

Four of our students have been selected by **MEDHASERVO** with a package of **5.25 LPA**


DIVYA SREE HARSHITHA


SWATHI


RAJESWARI


PRANEETH

Four of our students have been selected by **CAPGEMINI** with a package of **4 LPA**

BHUVANESWARI

IRSHAD

SWATHI

NAVEEN

SATHVIKA ZETA

SPECIAL ACHIVEMENTS

A stellar achievement to note is the selection of our esteemed ECE students for the prestigious **LAKSHMI** and **KEATS** Merit Scholarship. The recipients of this honor include ***Ramya, Joshna, Reshma, Sowjanya, Anusree, Anuradha, Sharmila, and Dakshayani***. This remarkable accomplishment is a testament to their academic excellence and dedication to their craft.

CERTIFICATIONS/PUBLICATIONS

- *Guda Yaswanth Reddy was awarded the NPTEL "Elite+Silver" certification in CMOS Digital VLSI design, recognizing his outstanding performance in the course.*
- *Kopanti Naga Sruthi Suvarsha was awarded an NPTEL certification in Fiber Optic Communication Technology upon successful completion of the course.*
- *Vana Tharun Kumar earned an NPTEL certification in Programming, Data Structures and Algorithms Using Python upon successful completion of the course.*
- *Swathi Neelisetty was awarded an NPTEL certification in Digital Circuits upon successful completion of the course.*
- *Tummalapalli Veera Venkata Jayasri earned an NPTEL certification in VLSI Design flow : RTL to GDS upon successful completion of the course.*
- *Katta Satish was awarded an NPTEL certification in VLSI Design flow : RTL to GDS upon successful completion of the course.*
- *Prapthi Chatterjee earned an NPTEL certification in The Joy Of Computing Using Python upon successful completion of the course.*
- *P. Eswar presented a paper titled "Digital Differentiator Design Using L1-Norm and LS with GOA Algorithm" in 22nd OITS International Conferencs on Information Technology at SRM university.*
- *P. Omkaram Published a patent "Echo Sound Based 360 Degrees Surveillance RADAR System", Patent No. 202541008308, Patent Journal No. 07/2025.*

SPORTS & ENGAGEMENT

G Srivallika

Secured **SILVER MEDAL** in the event 400 M at JNTUK Kakinada Central Zone University Athletics Meet.

Selected for **All India Inter-University Athletics** Meet.

Selected for **South Zone Inter-University Kho Kho Tournament**.

KaliPrasad and **Venu Naik Ramavath**

Selected for the JNTUK Inter-Collegiate Handball Tournament

IEEE EVENTS PUBLISHED

TEAR SHEETS

సాక్షి కాకినాడ

మంగళవారం : 4 : ఫిబ్రవరి : 2025

ప్రతి అధ్యాపకుడు నిరంతర విద్యార్థే

శిలాఫలకం ఆవిష్కరించిన ఇన్‌ఛార్జి వీసీ మురళీకృష్ణ తదితరులు

ఈసీఈ గ్యాలరీ హాల్‌లో మధురస్మృతులు

ఉపకార వేతనాల పంపిణీ

బాలాజీచెరువు: జేఎన్‌టీయూకే సివిల్ విభాగంలో బీటెక్ విద్యార్థులు 51 మందికి కాకినాడ ఇంజినీరింగ్ అలూమ్ని ట్రస్ట్, లక్ష్మీ ఫౌండేషన్లు కలిపి మంగళవారం ఉపకార వేతనాలు అందజేశారు. ముఖ్య అతిథులుగా ఇన్‌ఛార్జి వీసీ మురళీకృష్ణ, సివిల్ విభాగాధిపతి కోటేశ్వరరావు, ఇన్‌ఛార్జి ప్రిన్సిపాల్ మోహనరావు హాజరై వారికి అందజేసారు. విద్యార్థికి రూ.20 వేల చొప్పున 21 మందికి లక్ష్మీ ఫౌండేషన్, అలూమ్ని ట్రస్ట్ ద్వారా 30 మందికి రూ.20 వేల చొప్పున సమకూర్చగా వీటిని విద్యార్థులకు పంపిణీ చేసారు. ఈ కార్యక్రమంలో ట్రస్టీలు కె.రమేష్, ఎం.సంపత్‌కుమార్ పాల్గొన్నారు.

సూర్య

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కాకినాడ జిల్లా | తూర్పుగోదావరి జిల్లా
కోనసీమ జిల్లా | అల్లూరి సీతారామరాజు జిల్లా

శనివారం,15 మార్చి 2025

రోబోటిక్ అప్లికేషన్ల అభివృద్ధి, ఎంబెడెడ్ బోర్డుల రూపకల్పనపై శిక్షణా కార్యక్రమం

రూ.10 లక్షల విలువైన ఉపకార వేతనాలు

విద్యార్థులకు అందజేస్తున్న ఇన్‌ఛార్జి వీసీ తదితరులు

గాంధీనగర్: యూసీఈకే లో ఇంజినీరింగ్ చదువుతున్న 30 మంది పేద విద్యార్థులకు కీట్స్ సంస్థ ద్వారా, 21 మందికి లక్ష్మీ ఫౌండేషన్ ద్వారా రూ.20 వేల చొప్పున ఉపకార వేతనాలు అందించారు. కాకినాడలోని కళాశాలలో మంగళవారం జరిగిన కార్యక్రమంలో వీటిని అందజేశారు. కాకినాడ ఇంజినీరింగ్ అలూమ్ని ట్రస్టు ఫర్ సర్వీసెస్(కీట్స్), లక్ష్మీ ఫౌండేషన్‌ల సంయుక్తంగా ఈ మొత్తాన్ని అందించాయి. యూసీఈకే వైస్ ప్రిన్సిపల్ ఎం.స్వరూపారాణి అధ్యక్షతన జరిగిన కార్యక్రమానికి ఇన్‌ఛార్జి వీసీ మురళీకృష్ణ ముఖ్యఅతిథిగా హాజరయ్యారు. ఈ సందర్భంగా ఆయన మాట్లాడుతూ అమెరికాలోని విశ్వవిద్యాలయాల్లో ప్రవేశాలకు సంబంధించి విద్యార్థులకు కీట్స్ సంస్థ మార్గదర్శకం చేస్తోందన్నారు. ఓఎస్‌డీ డి.కోటేశ్వరరావు, కీట్స్ ఎగ్జిక్యూటివ్ ట్రస్టీలు కె.రమేష్, ఎం.సంపత్‌కుమార్‌లు మాట్లాడుతూ ప్రతిభావంతులను ప్రోత్సహించాలనే ఉద్దేశంతో వీటిని అందజేస్తున్నామన్నారు. ఐక్యూఏసీ ఇన్‌ఛార్జి డైరెక్టర్ ఎం.హెచ్.ఎం.కృష్ణప్రసాద్, ఇన్‌ఛార్జి ప్రిన్సిపల్ ఎన్.మోహనరావు తదితరులు పాల్గొన్నారు.

సూర్య

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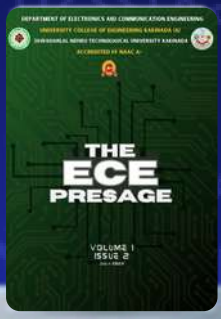

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